

DATA SHEET

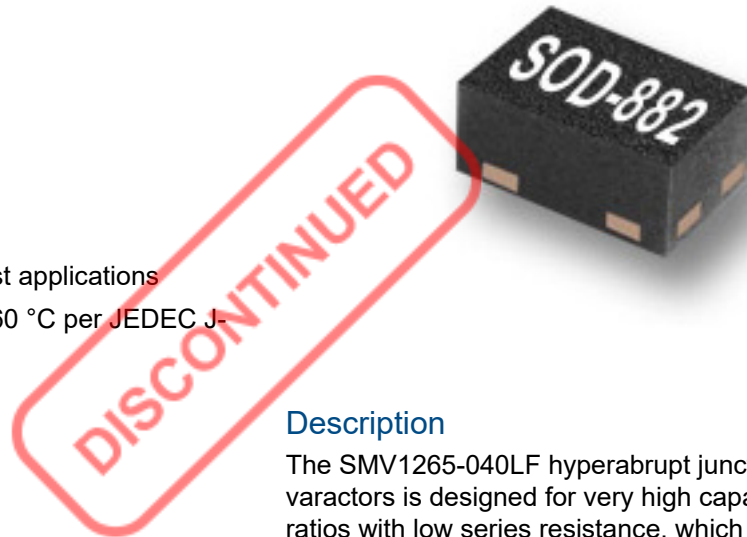
SMV1265-040LF: Hyperabrupt Junction Tuning Varactor

Applications

- Wideband RF and microwave VCOs
- Analog phase shifters
- Digital TV tuners

Features

- High tuning ratio
- Low series resistance
- Designed for high-volume, low-cost applications
- Small footprint package (MSL1, 260 °C per JEDEC J-STD-020)



Description

The SMV1265-040LF hyperabrupt junction tuning varactors is designed for very high capacitance tuning ratios with low series resistance, which makes this device especially attractive for wideband voltage controlled oscillator (VCO) applications.

The packaging and marking information is shown in Table 1. The absolute maximum ratings of the SMV1265-040LF are provided in Table 2. Electrical specifications are specified in Table 3. Figure 1 shows the typical performance of capacitance versus voltage. The SPICE model for the SMV1265-040LF is shown in Figure 2 and the associated model parameters are provided in Table 4. The relationship between voltage and capacitance for the SMV1265-040LF is shown in Table 5.

Table 1. Packaging and Marking


Single
SOD-882 Green™
SMV1265-040LF Marking: HD1
Ls = 0.45 nH



The Pb-free symbol or "LF" in the part number denotes a lead-free, RoHS-compliant package unless otherwise noted as Green™. Tin/lead (Sn/Pb) packaging is not recommended for new designs.

Table 2. SMV1265-040LF Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Power dissipation	P_{DIS}			250	mW
Forward current	I_F			20	mA
Operating temperature	T_{OP}	-55		+125	°C
Storage temperature	T_{STG}	-55		+150	°C

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. These devices must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SMV1265-040LF Electrical Specifications (Note 1)
($T_{OP} = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Reverse leakage current	I_R	$V_R = 26\text{ V}$			20	nA
Reverse breakdown voltage	V_{BR}	$I_R = 10\text{ }\mu\text{A}$	28			V
Capacitance	C_{T1}	$V_R = 1\text{ V}, f = 1\text{ MHz}$	12.5	13.8	14.7	pF
	C_{T26}	$V_R = 26\text{ V}, f = 1\text{ MHz}$	0.58	0.70	0.83	pF
Capacitance ratio	C_{T1}/C_{T26}	$C_T(1\text{ V})/C_T(26\text{ V})$	17.7	19.5		—
Series resistance	R_S	$V_R = 1\text{ V}, f = 470\text{ MHz}$		2.4		Ω

Note 1: Performance is guaranteed only under the conditions listed in this table.

Typical Performance Characteristics

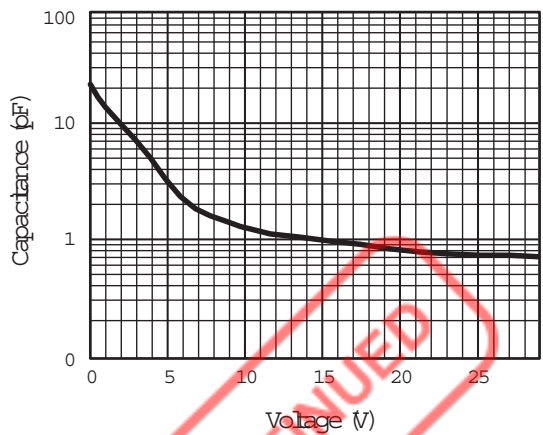


Figure 1. Capacitance vs Voltage @ 25 °C

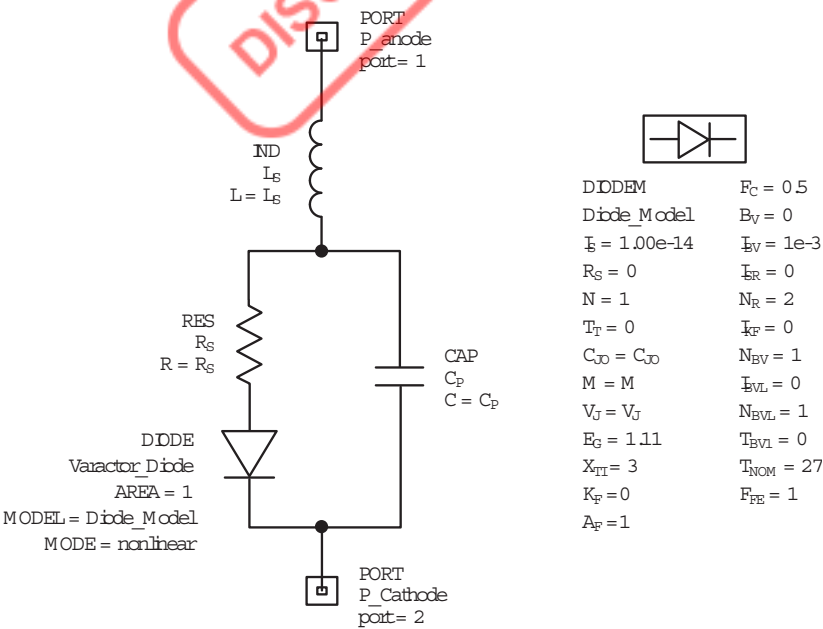


Figure 2. SPICE Model

Table 4. SPICE Model Parameters

Part Number	CJO (pF)	VJ (V)	M	CP (pF)	Rs (Ω)	Ls (nH)
SMV1265-040LF	22.5	30	13	0.71	2.4	0.45

Table 5. SMV1265-040LF Voltage vs Capacitance

Voltage (V_R) (V)	Typical Capacitance (C_T) (pF)
0	22.47
0.5	17.41
1.0	14.26
2.0	10.23
3.0	7.40
4.0	5.15
5.0	3.38
6.0	2.37
7.0	1.86
8.0	1.61
9.0	1.45
10	1.30
12	1.12
14	1.05
16	0.97
18	0.91
20	0.83
22	0.78
24	0.75
26	0.73
28	0.73
30	0.71

Package and Handling Information

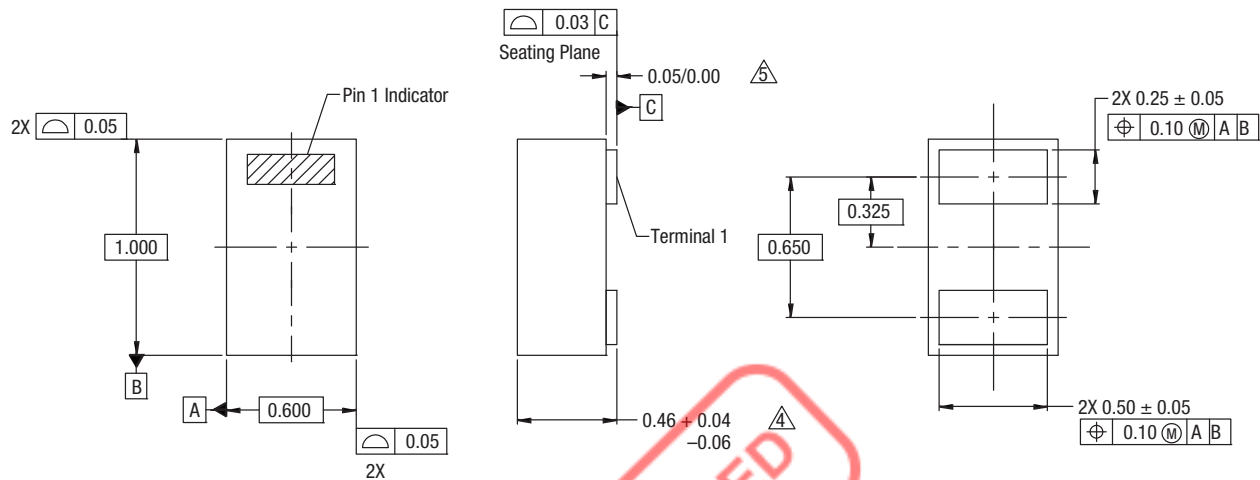
The package dimensions and tape and reel dimensions are shown in Figure 3 and 4, respectively.

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMV1265-040LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. They can be used for lead

or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

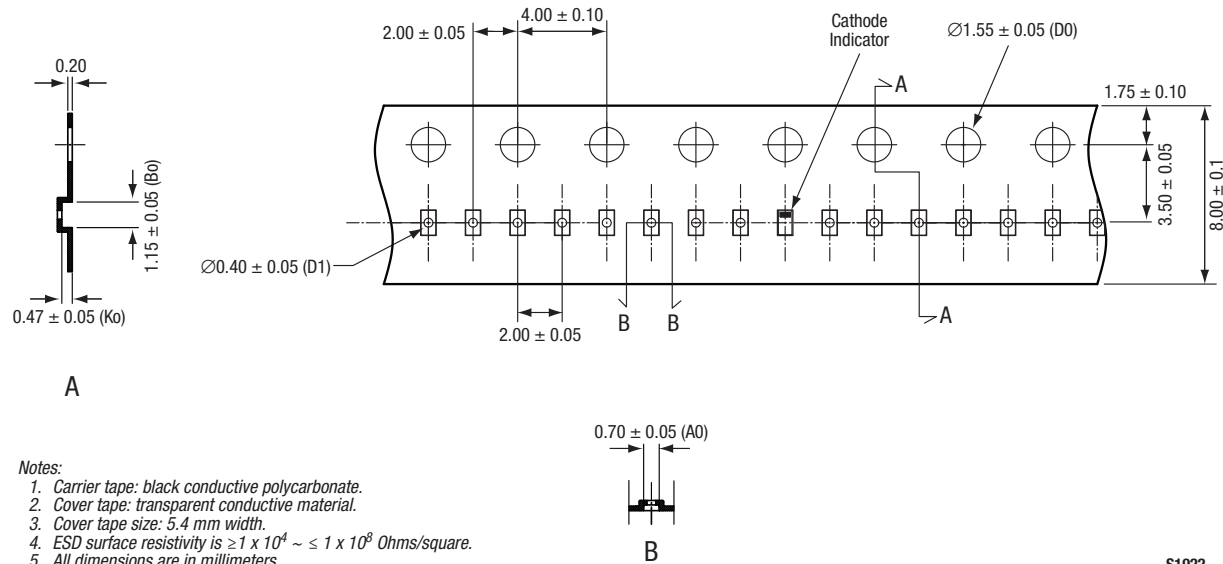


NOTES:

1. All measurements are in millimeters.
2. Dimensions and tolerances according to ASME Y14.5M-1994.
3. These packages are used principally for discrete devices.
4. This dimension includes stand-off height and package body thickness, but does not include attached features, e.g., external heatsink or chip capacitors. An integral heatslug is not considered an attached feature.
5. This dimension is primarily terminal plating, but does not include small metal protrusion.

Y1410

Figure 3. SOD-882 Package Dimensions



Notes:

1. Carrier tape: black conductive polycarbonate.
2. Cover tape: transparent conductive material.
3. Cover tape size: 5.4 mm width.
4. ESD surface resistivity is $\geq 1 \times 10^4 \sim \leq 1 \times 10^8$ Ohms/square.
5. All dimensions are in millimeters.

S1922

Figure 4. SOD-882 Tape and Reel Dimensions



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