

CENTERLINE OF CONNECTOR

CIRCUIT #1
DESIGNATOR

2080211000 WITH
PICK AND PLACE DUST CAP
(SCALE 8:1)

DDDY JULIAN DATE CODE
AND PART NUMBER
1.50 ± 0.25
LASER ETCHED
HIGH CHARACTERS

DDDY

2080211000

CIRCUIT #1
DESIGNATOR

CIRCUIT #1

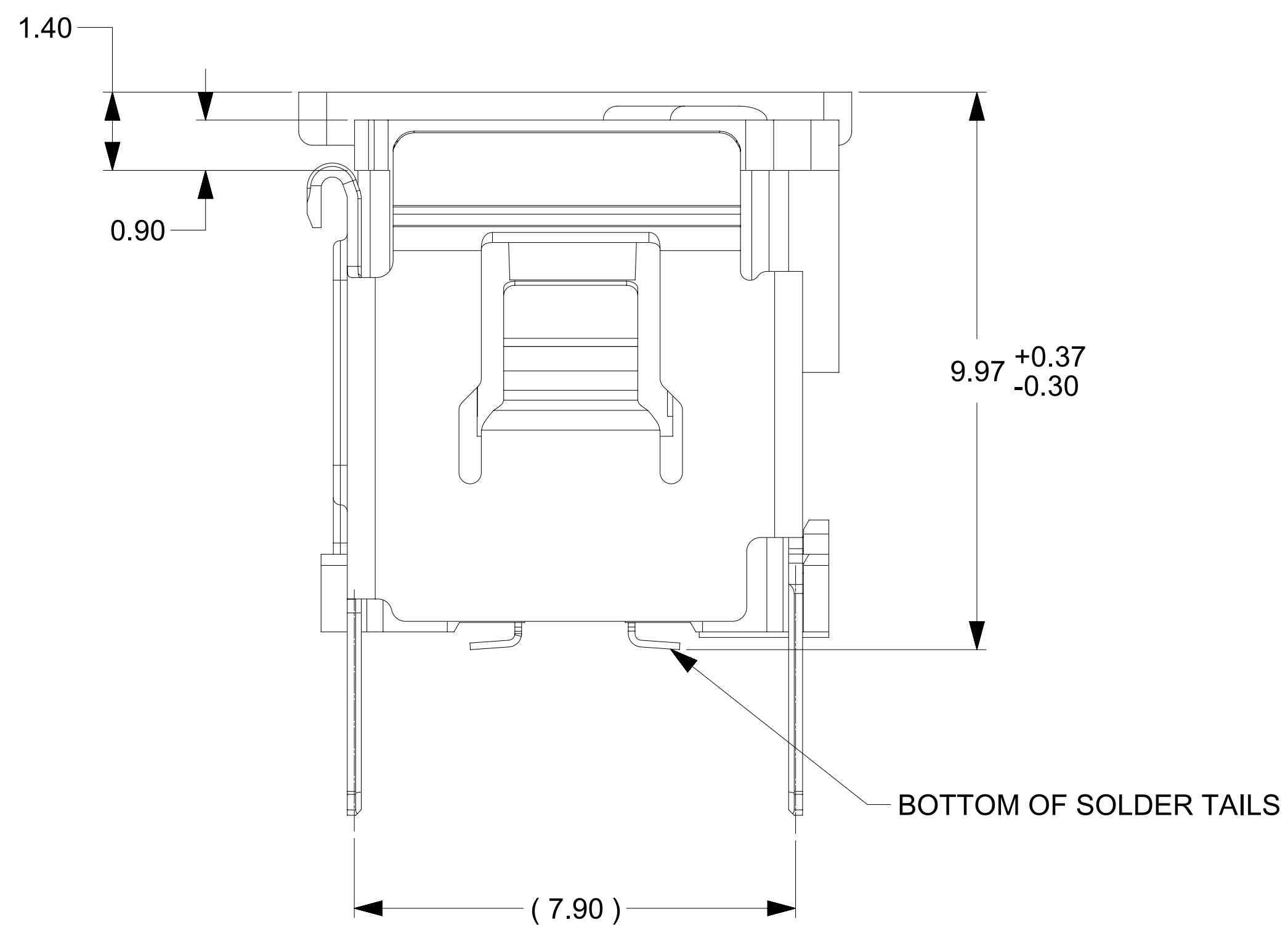
4X 0.60 +0.02
-0.06

NOTES:

- MATERIAL:
HOUSING: LCP, GLASS FILLED, UL 94V-0 BLACK
PICK AND PLACE DUST COVER: LCP, GLASS FILLED, UL 94V-0 NATURAL
CONTACT: HIGH PERFORMANCE COPPER ALLOY
CAN: STAINLESS STEEL
- FINISH:
CONTACT: 30 MICROINCHES (0.76 MICRONS) MIN SELECTIVE GOLD ON CONTACT AREA,
1.97 -13.78 MICROINCHES (0.05 - 0.35 MICRONS) GOLD FLASH OVER 50 MICROINCHES
(1.27 MICRONS) NICKEL MIN OVERALL.
CAN: 1.25 MICRONS NICKEL MIN OVERALL
CAN PC TAIL: 16-35 MICRONS HOT TIN DIP
- PRODUCT SPECIFICATION: 2080210001-PS
- PACKAGING SPECIFICATION: 2080219000
- APPLICATION SPECIFICATION: 2080210001-AS
- MATES TO NEARSTACK PCIe SERIES:
RIGHT ANGLE: 216070, 216711, 219246, 219333, 220929, 220930, 216710, 220931, 220932
ANGLE EXIT: 217033, 216720, 218800, 220933, 220934, 217268, 220935, 220936
VERTICAL EXIT: 216644, 219345, 220254, 220753, 217575, 219344, 220265, 220754, 219687, 218411
- MUST CONFORM TO THE COSMETIC SPECIFICATION: PS-45499-002, CLASS B; EXCEPT
ANY IMPERFECTIONS MUST FIT WITHIN A 0.08mm DIAMETER SPHERE
(DUE TO THE SMALL SIZE OF THIS PART).

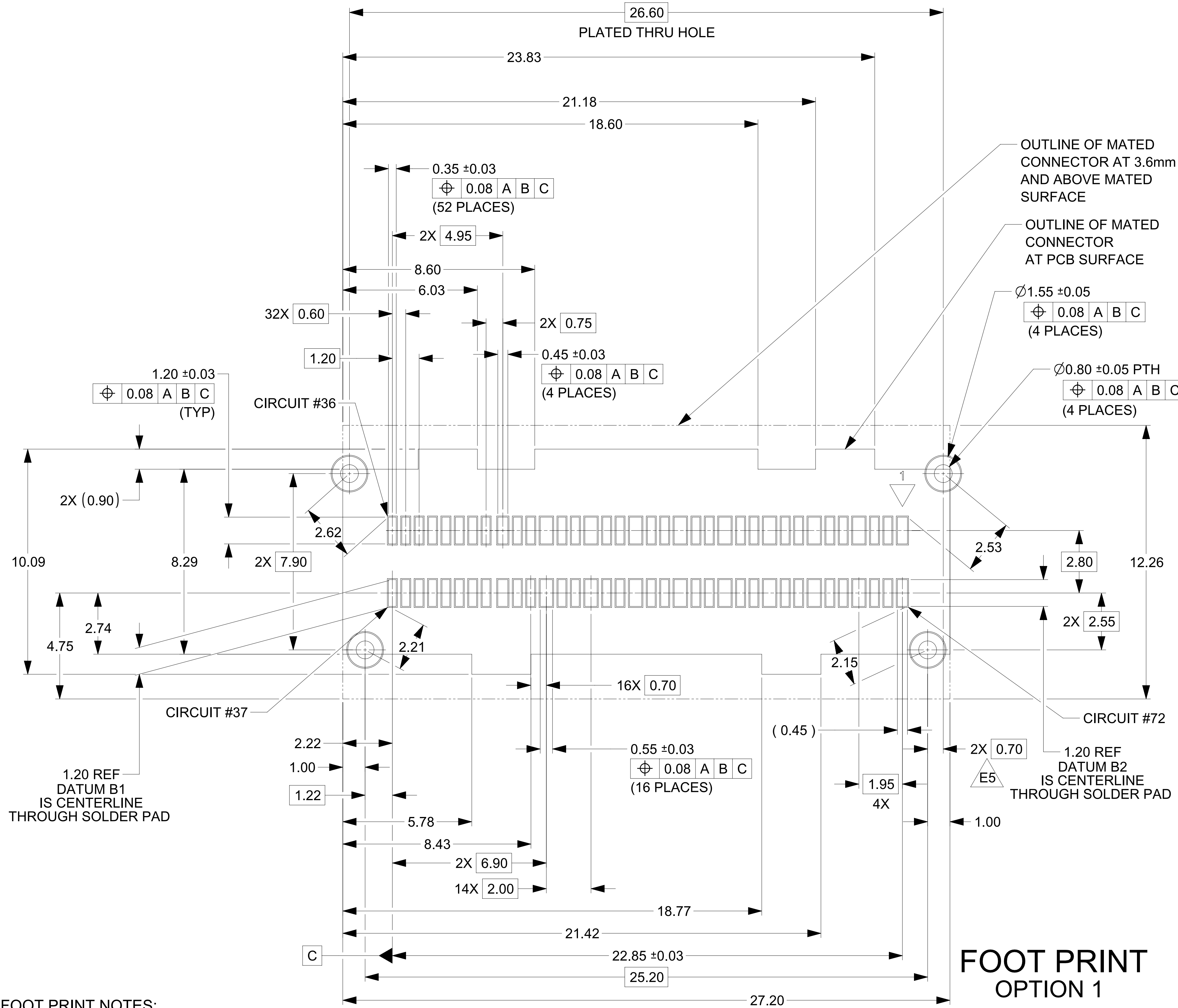
PART NUMBER	DIM -A-	DUST CAP
2080211000	2.96 mm	YES
2080211100	1.59 mm	
2080215000	2.96 mm	NO
2080215100	1.59 mm	

FUNCTIONAL SYMBOLS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX ELECTRONIC TECHNOLOGIES, LLC AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION													
	DIMENSION UNITS		SCALE		CURRENT REV DESC: Add additional finished goods part number					molex PLUG, NEARSTACK PCIE 18DP & 16SE PRODUCT SALES DRAWING				
	MM		10:1											
	GENERAL TOLERANCES (UNLESS SPECIFIED)													
	ANGULAR TOL ± 0.5°				STATUS: Production DRWN: Martin Fan 2024-03-29 CHK'D: Edward Liao 2024-05-07 APPR: Benny He 2024-05-07					DOCUMENT NUMBER		DOC TYPE	DOC PART	REVISION
	4 PLACES		±							2080211000		PSD	000	F
	3 PLACES		±											
	2 PLACES		± 0.200											
	1 PLACE		±											
	0 PLACES		±											
DIVISIONAL SYMBOLS		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIRD ANGLE PROJECTION		DRAWING	SERIES	MATERIAL NUMBER	CUSTOMER		SHEET NUMBER			
						D-SIZE	208021	SEE TABLE	GENERAL MARKET		1 OF 6			



FUNCTIONAL SYMBOLS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX ELECTRONIC TECHNOLOGIES, LLC AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION										
	DIMENSION UNITS		SCALE	CURRENT REV DESC: Add additional finished goods part number	molex						
	MM	12:1									
	GENERAL TOLERANCES (UNLESS SPECIFIED)										
ANGULAR TOL		±	0.5°								
DIVISIONAL SYMBOLS	4 PLACES		±	STATUS: Production DRWN: Martin Fan 2024-03-29 CHK'D: Edward Liao 2024-05-07 APPR: Benny He 2024-05-07	PLUG, NEARSTACK PCIE 18DP & 16SE						
	3 PLACES		±								
	2 PLACES		±		0.200	PRODUCT SALES DRAWING					
	1 PLACE		±								
	0 PLACES		±								
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				THIRD ANGLE PROJECTION		DRAWING	SERIES	MATERIAL NUMBER	CUSTOMER	SHEET NUMBER
					D-SIZE	208021	SEE TABLE	GENERAL MARKET	2 OF 6		

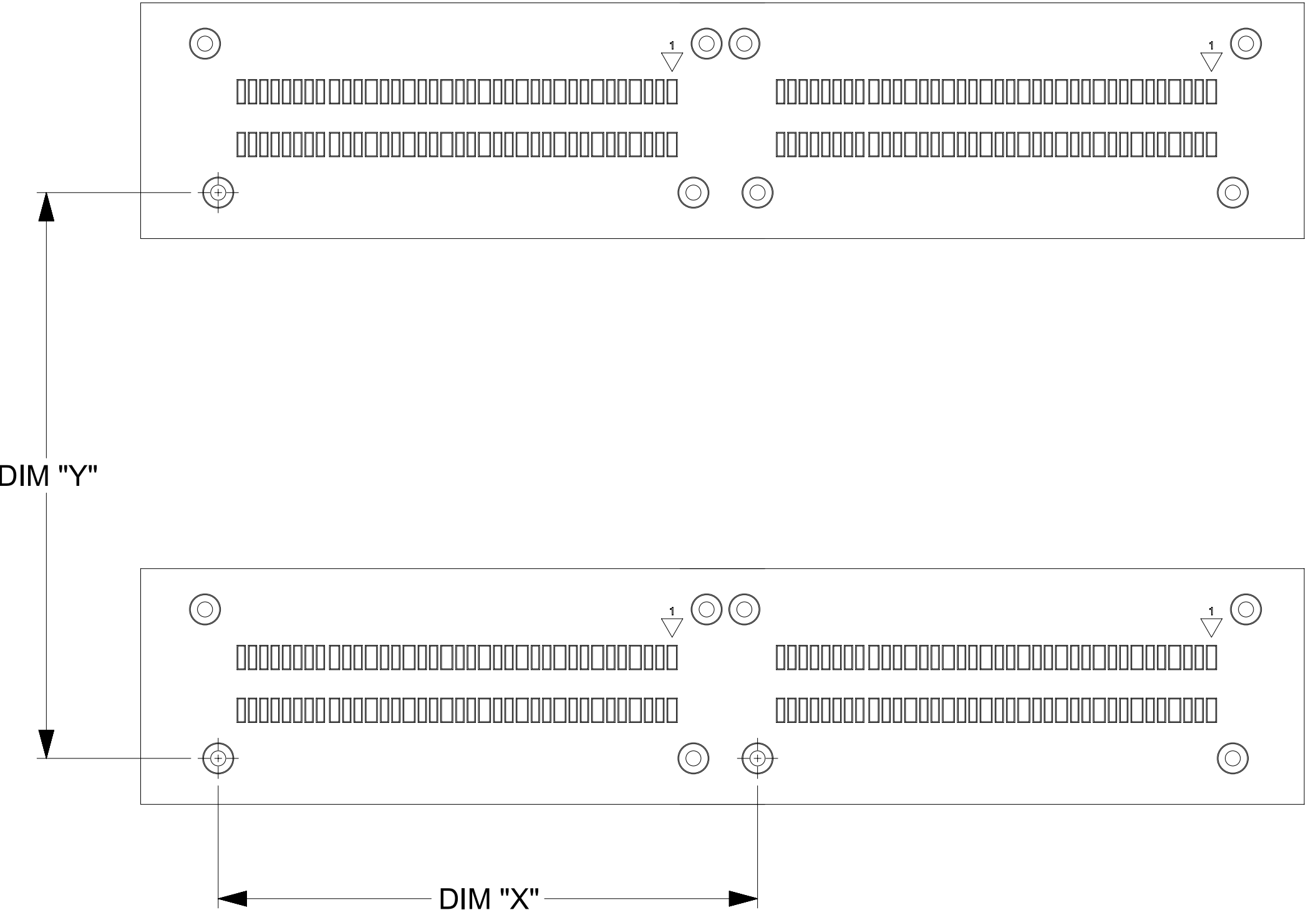
CABLES EMERGE FROM THIS SIDE OF THE MATED CONNECTORS



FOOT PRINT NOTES:
1. DATUM -A- IS THE TOP OF THE PCB.
2. PIN-IN-PASTE SOLDERING METHOD IS RECOMMENDED, INCLUDING THRU-HOLE CAN TAILS. CONSULT NEARSTACK PCIe APPLICATION SPECIFICATION 2080210001-AS FOR FURTHER DETAILS ON SMT SOLDERING PROCESS RECOMMENDATIONS.

SPACING RECOMMENDATIONS FOR MATING RECEPTACLE CABLE ASSEMBLIES SHOWN BELOW. CONSULT NEARSTACK PCIe DESIGN GUIDE 2080210002-AS FOR FURTHER DETAILS ON MINIMUM CONNECTOR SPACING.

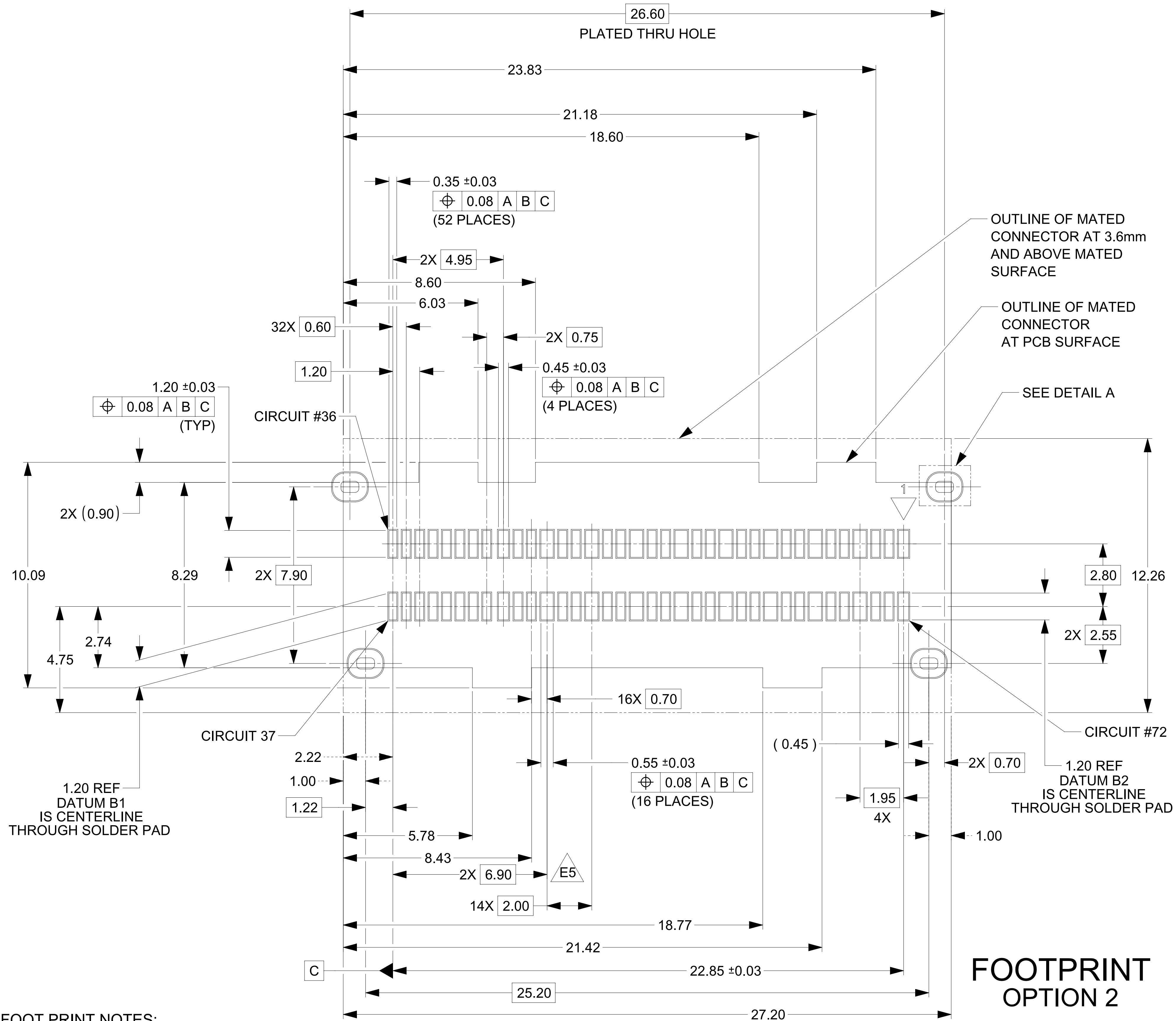
MATING CABLE SERIES	DESCRIPTION	DIM "X"	DIM "Y"
216070	HORIZONTAL EXIT WITH PULL TAB	28.60	30.00
216710	HORIZONTAL EXIT NON-PULL TAB	34.50	30.00
217033	ANGLE EXIT WITH PULL TAB	28.60	15.00
217268	ANGLE EXIT NON-PULL TAB	34.50	15.00
216644	VERTICAL EXIT WITH PULL TAB	28.60	12.00



FOOT PRINT
OPTION 1

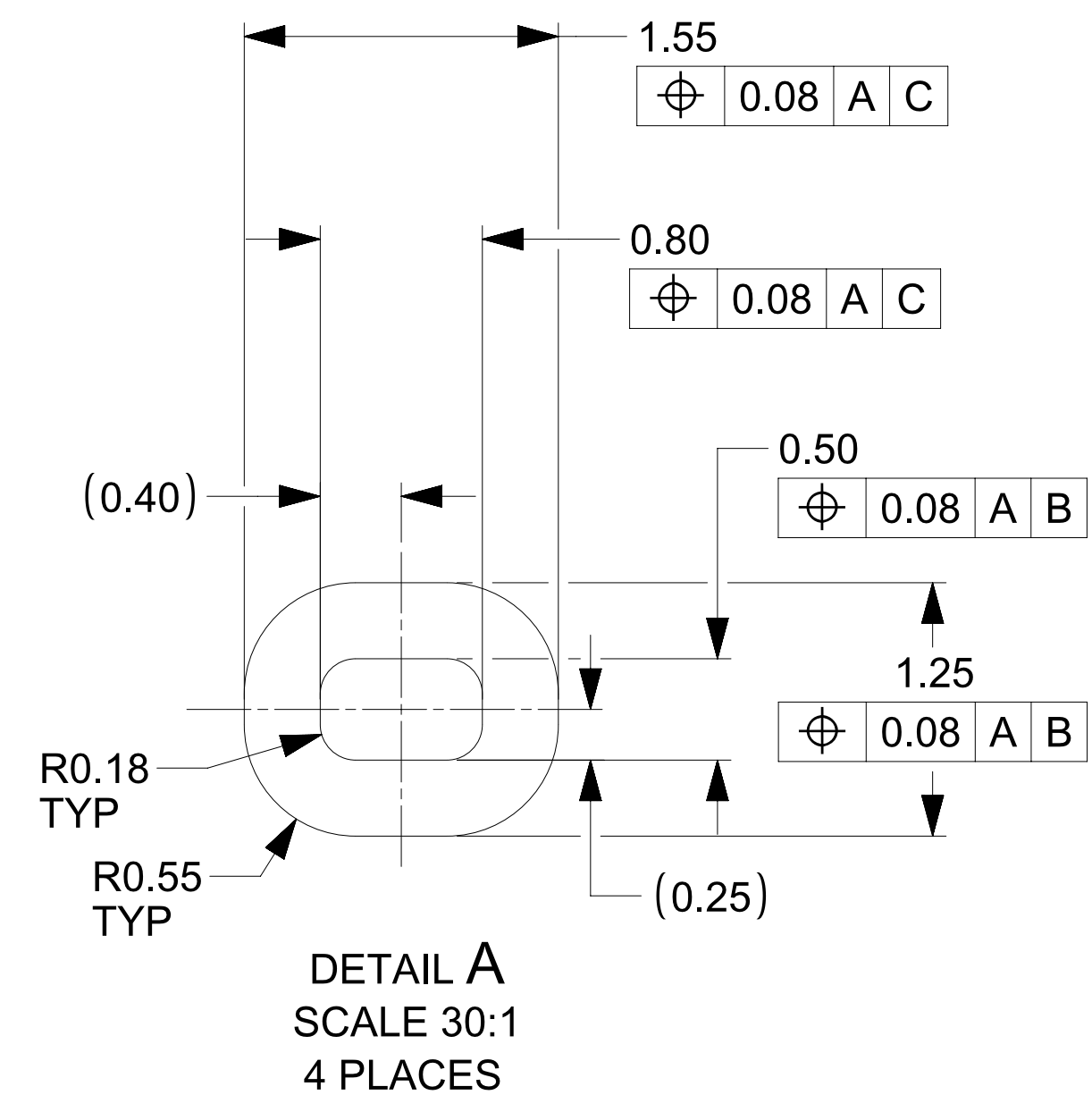
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX ELECTRONIC TECHNOLOGIES, LLC AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																	
FUNCTIONAL SYMBOLS E/A = 0 E/C = 0 E/P = 0 DIVISIONAL SYMBOLS		DIMENSION UNITS		SCALE		CURRENT REV DESC: Add additional finished goods part number			molex PLUG, NEARSTACK PCIE 18DP & 16SE								
		MM		10:1													
		GENERAL TOLERANCES (UNLESS SPECIFIED)				PRODUCT SALES DRAWING DOCUMENT NUMBER2080211000DOC TYPEPSDDOC PART000REVISIONF											
		ANGULAR TOL ± 0.5°															
		4 PLACES		±								STATUS: Production DRWN: Martin Fan2024-03-29 CHK'D: Edward Liao2024-05-07 APPR: Benny He2024-05-07					
		3 PLACES		±													
2 PLACES		± 0.200															
1 PLACE		±		MATERIAL NUMBER SEE TABLE													
0 PLACES		±								CUSTOMER GENERAL MARKET							
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				THIRD ANGLE PROJECTION 		DRAWING D-SIZE		SERIES 208021								SHEET NUMBER 3 OF 6	

CABLES EMERGE FROM THIS SIDE OF THE MATED CONNECTORS



FOOT PRINT NOTES:

1. DATUM -A- IS THE TOP OF THE PCB.
2. PIN-IN-PASTE SOLDERING METHOD IS RECOMMENDED, INCLUDING THRU-HOLE CAN TAILS. CONSULT NEARSTACK PCIe APPLICATION SPECIFICATION 2080210001-AS FOR FURTHER DETAILS ON SMT SOLDERING PROCESS RECOMMENDATIONS.



FUNCTIONAL SYMBOLS F A = 0 F C = 0 F E = 0 DIVISIONAL SYMBOLS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX ELECTRONIC TECHNOLOGIES, LLC AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION												
	DIMENSION UNITS		SCALE		CURRENT REV DESC: Add additional finished goods part number			molex					
	MM		1:1										
	GENERAL TOLERANCES (UNLESS SPECIFIED)												
	ANGULAR TOL ± 0.5°												
	4 PLACES		±										
	3 PLACES		±										
	2 PLACES		± 0.200										
	1 PLACE		±										
	0 PLACES		±										
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIRD ANGLE PROJECTION		DRAWING		SERIES		MATERIAL NUMBER		CUSTOMER		SHEET NUMBER	
				D-SIZE		208021		SEE TABLE		GENERAL MARKET		4 OF 6	

	CIRCUIT #		CIRCUIT #
GROUND 1	1	GROUND 2	72
SIGNAL (DIFF PAIR)	2	SIGNAL (DIFF PAIR)	71
SIGNAL (DIFF PAIR)	3	SIGNAL (DIFF PAIR)	70
GROUND 1	4	GROUND 2	69
SIGNAL (DIFF PAIR)	5	SIGNAL (DIFF PAIR)	68
SIGNAL (DIFF PAIR)	6	SIGNAL (DIFF PAIR)	67
GROUND 1	7	GROUND 2	66
SIGNAL (DIFF PAIR)	8	SIGNAL (DIFF PAIR)	65
SIGNAL (DIFF PAIR)	9	SIGNAL (DIFF PAIR)	64
GROUND 1	10	GROUND 2	63
SIGNAL (DIFF PAIR)	11	SIGNAL (DIFF PAIR)	62
SIGNAL (DIFF PAIR)	12	SIGNAL (DIFF PAIR)	61
GROUND 1	13	GROUND 2	60
SIGNAL (DIFF PAIR)	14	SIGNAL (DIFF PAIR)	59
SIGNAL (DIFF PAIR)	15	SIGNAL (DIFF PAIR)	58
GROUND 1	16	GROUND 2	57
SIGNAL (DIFF PAIR)	17	SIGNAL (DIFF PAIR)	56
SIGNAL (DIFF PAIR)	18	SIGNAL (DIFF PAIR)	55
GROUND 1	19	GROUND 2	54
SIGNAL (DIFF PAIR)	20	SIGNAL (DIFF PAIR)	53
SIGNAL (DIFF PAIR)	21	SIGNAL (DIFF PAIR)	52
GROUND 1	22	GROUND 2	51
SIGNAL (DIFF PAIR)	23	SIGNAL (DIFF PAIR)	50
SIGNAL (DIFF PAIR)	24	SIGNAL (DIFF PAIR)	49
GROUND 1	25	GROUND 2	48
SIGNAL (DIFF PAIR)	26	SIGNAL (DIFF PAIR)	47
SIGNAL (DIFF PAIR)	27	SIGNAL (DIFF PAIR)	46
GROUND 1	28	GROUND 2	45
SIGNAL (SINGLE ENDED)	29	SIGNAL (SINGLE ENDED)	44
SIGNAL (SINGLE ENDED)	30	SIGNAL (SINGLE ENDED)	43
SIGNAL (SINGLE ENDED)	31	SIGNAL (SINGLE ENDED)	42
SIGNAL (SINGLE ENDED)	32	SIGNAL (SINGLE ENDED)	41
SIGNAL (SINGLE ENDED)	33	SIGNAL (SINGLE ENDED)	40
SIGNAL (SINGLE ENDED)	34	SIGNAL (SINGLE ENDED)	39
SIGNAL (SINGLE ENDED)	35	SIGNAL (SINGLE ENDED)	38
SIGNAL (SINGLE ENDED)	36	SIGNAL (SINGLE ENDED)	37

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FUNCTIONAL SYMBOLS  A = 0  C = 0  P = 0 DIVISIONAL SYMBOLS		DIMENSION UNITS SCALE MM 1:1		CURRENT REV DESC: Add additional finished goods part number			molex PLUG, NEARSTACK PCIE 18DP & 16SE						
		GENERAL TOLERANCES (UNLESS SPECIFIED) ANGULAR TOL ± 0.5°			STATUS: Production DRWN: Martin Fan 2024-03-29 CHK'D: Edward Liao 2024-05-07 APPR: Benny He 2024-05-07								
		4 PLACES ±											
		3 PLACES ±											
2 PLACES ± 0.200					DOCUMENT NUMBER		DOC TYPE		DOC PART		REVISION		
1 PLACE ±					2080211000		PSD		000		F		
0 PLACES ±													
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIRD ANGLE PROJECTION		DRAWING		SERIES		MATERIAL NUMBER		CUSTOMER		SHEET NUMBER	
				D-SIZE		208021		SEE TABLE		GENERAL MARKET		5 OF 6	

REVISION HISTORY			
DATE	REVISION	DESCRIPTION	ENGINEER
2024/01/31	E5	@SHEET 1 1. ADD ADDITIONAL PART NUMBER 2080215000 & 2080215100 @B12 2. CHANGE FROM 1.50 TO 1.59 @C10 (ERROR CORRECTION) @SHEET 3 1. CHANGE TO BASIC DIMENTION @D10 @SHEET 4 1. CHANGE QUANTITY TO 2X @C14	MARTIN FAN
2024/03/29	F	CANCEL THE DESIGN OPTIMIZTION OF CAGE AND WAFER	MARTIN FAN

FUNCTIONAL SYMBOLS

F

A

=

0

F

C

=

0

F

P

=

0

DIVISIONAL SYMBOLS

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DIMENSION UNITS

MM

SCALE

1:1

GENERAL TOLERANCES (UNLESS SPECIFIED)

ANGULAR TOL ± 0.5°

4 PLACES ±

3 PLACES ±

2 PLACES ± 0.200

1 PLACE ±

0 PLACES ±

DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS

CURRENT REV DESC: Add additional finished goods part number

STATUS: Production

DRWN: Martin Fan

CHK'D: Edward Liao

APPR: Benny He

THIRD ANGLE PROJECTION

DRAWING

D-SIZE

SERIES

208021

MATERIAL NUMBER

SEE TABLE

CUSTOMER

GENERAL MARKET

SHEET NUMBER

6 OF 6

2024-03-29

2024-05-07

2024-05-07

DOCUMENT NUMBER

2080211000

DOC TYPE

PSD

DOC PART

000

REVISION

F

molex

PLUG, NEARSTACK PCIE 18DP & 16SE

PRODUCT SALES DRAWING