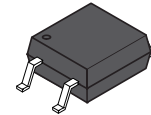


# 4-Pin Full Pitch Mini-Flat Package Zero-Cross Triac Driver Output Optocouplers

## FODM3063, FODM3083


MFP4 3.85X4.4, 2.54P  
CASE 100AP

### Description

The FODM3063 and FODM3083 series consist of an infrared emitting diode optically coupled to a monolithic silicon detector performing the function of a zero voltage crossing bilateral triac driver, and is housed in a compact 4-pin mini-flat package. The lead pitch is 2.54 mm. They are designed for use with a triac in the interface of logic systems to equipment powered from 115/240 VAC lines, such as solid state relays, industrial controls, motors, solenoids and consumer appliances.

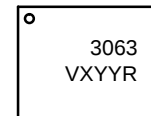
### Features

- Critical Rate of Rise of Off-Stage Voltage
  - ◆  $dv/dt$  of 600 V/ $\mu$ s Guaranteed
- Zero Voltage Crossing
- Peak Blocking Voltage
  - ◆ 600 V (FODM3063)
  - ◆ 800 V (FODM3083)
- Compact 4-Pin Surface Mount Package
  - ◆ 2.4 mm Maximum Standoff Height
- Safety Regulatory Approvals:
  - ◆ UL1577, 3,750 VAC<sub>RMS</sub> for 1 Minute
  - ◆ DIN-EN/IEC60747-5-5, 565 V Peak Working Insulation Voltage
- These are Pb-Free Devices

### Applications

- Solenoid/Valve Controls
- Lighting Controls
- Static Power Switches
- AC Motor Drives
- Temperature Controls
- E.M. Contactors
- AC Motor Starters
- Solid State Relays

### MARKING DIAGRAM



3063 = Device Number

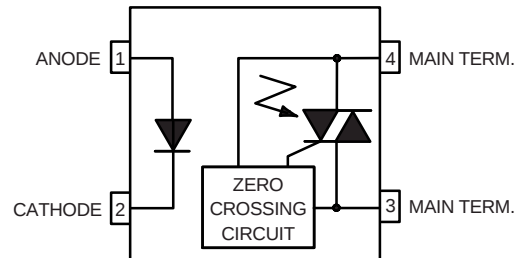
V = DIN EN/IEC60747-5-5 Option (Only Appears on Component Ordered with this Option)

X = One-Digit Year Code, e.g., "6"

YY = Digit Work Week, Ranging from "01" to "53"

R = Assembly Package Code

### FUNCTIONAL SCHEMATIC



### ORDERING INFORMATION

See detailed ordering and shipping information on page 9 of this data sheet.

# FODM3063, FODM3083

**SAFETY AND INSULATION RATINGS** (As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for “safe electrical insulation” only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.)

| Parameter                                                                           |                        | Characteristics |
|-------------------------------------------------------------------------------------|------------------------|-----------------|
| Installation Classifications per DIN VDE 0110/1.89 Table 1, For Rated Mains Voltage | < 150 V <sub>RMS</sub> | I–IV            |
|                                                                                     | < 300 V <sub>RMS</sub> | I–III           |
| Climatic Classification                                                             |                        | 40/100/21       |
| Pollution Degree (DIN VDE 0110/1.89)                                                |                        | 2               |
| Comparative Tracking Index                                                          |                        | 175             |

| Symbol                | Parameter                                                                                                                                                      | Value            | Unit              |
|-----------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------|-------------------|
| V <sub>PR</sub>       | Input-to-Output Test Voltage, Method A, V <sub>IORM</sub> × 1.6 = V <sub>PR</sub> , Type and Sample Test with t <sub>m</sub> = 10 s, Partial Discharge < 5 pC  | 904              | V <sub>peak</sub> |
|                       | Input-to-Output Test Voltage, Method B, V <sub>IORM</sub> × 1.875 = V <sub>PR</sub> , 100% Production Test with t <sub>m</sub> = 1 s, Partial Discharge < 5 pC | 1060             | V <sub>peak</sub> |
| V <sub>IORM</sub>     | Maximum Working Insulation Voltage                                                                                                                             | 565              | V <sub>peak</sub> |
| V <sub>IOTM</sub>     | Highest Allowable Over-Voltage                                                                                                                                 | 6000             | V <sub>peak</sub> |
|                       | External Creepage                                                                                                                                              | ≥5               | mm                |
|                       | External Clearance                                                                                                                                             | ≥5               | mm                |
| DTI                   | Distance Through Insulation (Insulation Thickness)                                                                                                             | ≥0.4             | mm                |
| T <sub>S</sub>        | Case Temperature (Note 1)                                                                                                                                      | 150              | °C                |
| I <sub>S,INPUT</sub>  | Input Current (Note 1)                                                                                                                                         | 200              | mA                |
| P <sub>S,OUTPUT</sub> | Output Power (Note 1)                                                                                                                                          | 300              | mW                |
| R <sub>IO</sub>       | Insulation Resistance at T <sub>S</sub> , V <sub>IO</sub> = 500 V (Note 1)                                                                                     | >10 <sup>9</sup> | Ω                 |

1. Safety limit values – maximum values allowed in the event of a failure.

## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

| Symbol           | Parameter               | Value        | Unit |
|------------------|-------------------------|--------------|------|
| T <sub>STG</sub> | Storage Temperature     | –55 to +150  | °C   |
| T <sub>OPR</sub> | Operating Temperature   | –40 to +100  | °C   |
| T <sub>J</sub>   | Junction Temperature    | –40 to +125  | °C   |
| T <sub>SOL</sub> | Lead Solder Temperature | 260 for 10 s | °C   |

### EMITTER

|                         |                                                                     |     |    |
|-------------------------|---------------------------------------------------------------------|-----|----|
| I <sub>F</sub> (avg)    | Continuous Forward Current                                          | 60  | mA |
| I <sub>F</sub> (pk)     | Peak Forward Current (1 μs Pulse, 300 pps.)                         | 1   | A  |
| V <sub>R</sub>          | Reverse Input Voltage                                               | 6   | V  |
| P <sub>D(EMITTER)</sub> | Power Dissipation (No Derating Required over Operating Temp. Range) | 100 | mW |

### DETECTOR

|                          |                                                                     |          |     |                     |
|--------------------------|---------------------------------------------------------------------|----------|-----|---------------------|
| I <sub>TSM</sub>         | Peak Non-Repetitive Surge Current (Single Cycle 60 Hz Sine Wave)    |          | 1   | A <sub>(PEAK)</sub> |
| I <sub>T(RMS)</sub>      | On-State RMS Current                                                |          | 70  | mA <sub>(RMS)</sub> |
| V <sub>DRM</sub>         | Off-State Output Terminal Voltage                                   | FODM3063 | 600 | V                   |
|                          |                                                                     | FODM3083 | 800 | V                   |
| P <sub>D(DETECTOR)</sub> | Power Dissipation (No Derating Required over Operating Temp. Range) |          | 300 | mW                  |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# FODM3063, FODM3083

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Unit |
|--------|-----------|-----------------|-----|-----|-----|------|
|--------|-----------|-----------------|-----|-----|-----|------|

### INDIVIDUAL COMPONENT CHARACTERISTICS

|                  |                                            |                                                      |     |   |      |      |
|------------------|--------------------------------------------|------------------------------------------------------|-----|---|------|------|
| <b>EMITTER</b>   |                                            |                                                      |     |   |      |      |
| V <sub>F</sub>   | Input Forward Voltage                      | I <sub>F</sub> = 30 mA                               | –   | – | 1.50 | V    |
| I <sub>R</sub>   | Reverse Leakage Current                    | V <sub>R</sub> = 6 V                                 | –   | – | 100  | μA   |
| <b>DETECTOR</b>  |                                            |                                                      |     |   |      |      |
| I <sub>DRM</sub> | Peak Blocking Current Either Direction     | Rated V <sub>DRM</sub> , I <sub>F</sub> = 0 (Note 2) | –   | – | 500  | nA   |
| dv/dt            | Critical Rate of Rise of Off-State Voltage | I <sub>F</sub> = 0 (Note 3)                          | 600 | – | –    | V/μs |

### TRANSFER CHARACTERISTICS

|                 |                                         |                                                                           |   |     |   |    |
|-----------------|-----------------------------------------|---------------------------------------------------------------------------|---|-----|---|----|
| I <sub>FT</sub> | LED Trigger Current                     | Main Terminal Voltage = 3 V (Note 4)                                      | – | –   | 5 | mA |
| I <sub>H</sub>  | Holding Current, Either Direction       |                                                                           | – | 300 | – | μA |
| V <sub>TM</sub> | Peak On-State Voltage, Either Direction | I <sub>F</sub> = Rated I <sub>FT</sub> ,<br>I <sub>TM</sub> = 100 mA peak | – | –   | 3 | V  |

### ZERO CROSSING CHARACTERISTICS

|                   |                                                                      |                                                                              |   |   |    |    |
|-------------------|----------------------------------------------------------------------|------------------------------------------------------------------------------|---|---|----|----|
| V <sub>IH</sub>   | Inhibit Voltage, MT1–MT2 Voltage above which Device will not Trigger | I <sub>FT</sub> = Rated I <sub>FT</sub>                                      | – | – | 20 | V  |
| I <sub>DRM2</sub> | Leakage in Inhibit State                                             | I <sub>FT</sub> = Rated I <sub>FT</sub> , Rated V <sub>DRM</sub> , Off-State | – | – | 2  | mA |

### ISOLATION CHARACTERISTICS

|                  |                                         |                             |       |   |   |                    |
|------------------|-----------------------------------------|-----------------------------|-------|---|---|--------------------|
| V <sub>ISO</sub> | Steady State Isolation Voltage (Note 5) | 1 Minute, R.H. = 40% to 60% | 3,750 | – | – | VAC <sub>RMS</sub> |
|------------------|-----------------------------------------|-----------------------------|-------|---|---|--------------------|

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Test voltage must be applied within dv/dt rating.
3. This is static dv/dt. Commutating dv/dt is function of the load-driving thyristor(s) only.
4. All devices are guaranteed to trigger at an I<sub>F</sub> value less than or equal to max I<sub>FT</sub>. Therefore, recommended operating I<sub>F</sub> lies between max I<sub>FT</sub> (5 mA) and absolute max I<sub>F</sub> (60 mA).
5. Steady state isolation voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, pins 1 & 2 are common, and pins 3 & 4 are common.

TYPICAL PERFORMANCE CHARACTERISTICS

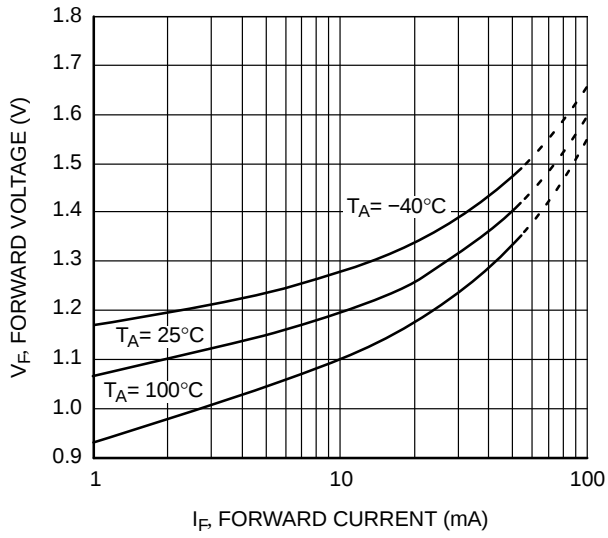


Figure 1. LED Forward Voltage vs. Forward Current

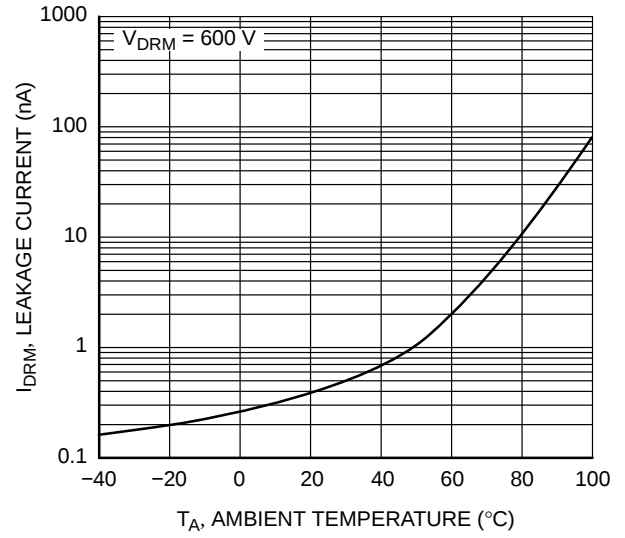


Figure 2. Leakage Current vs. Ambient Temperature

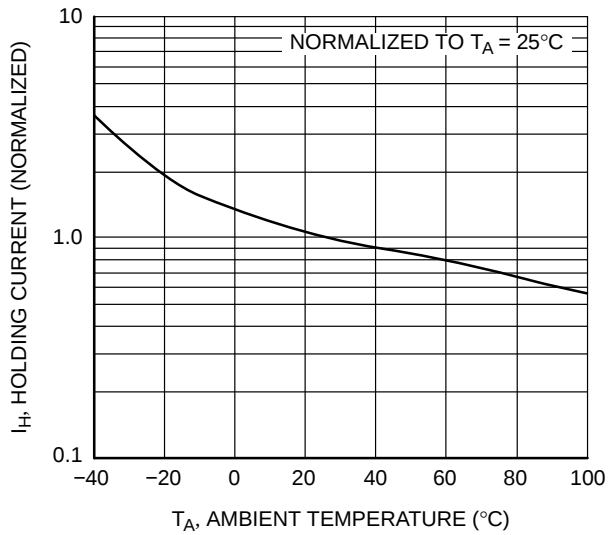


Figure 3. Holding Current vs. Ambient Temperature

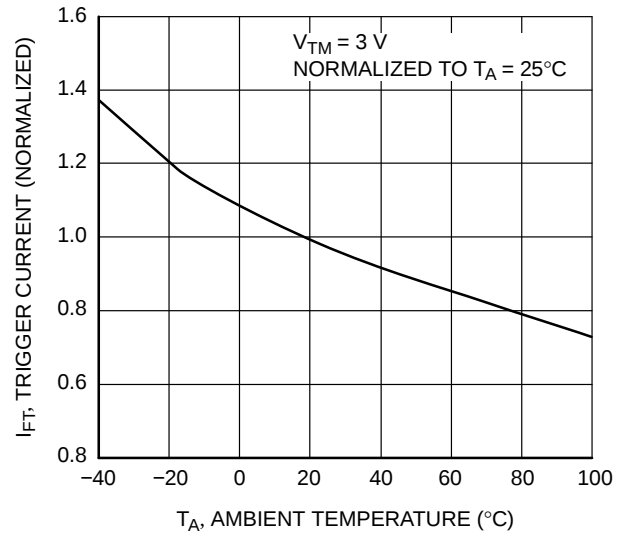


Figure 4. Trigger Current vs. Ambient Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

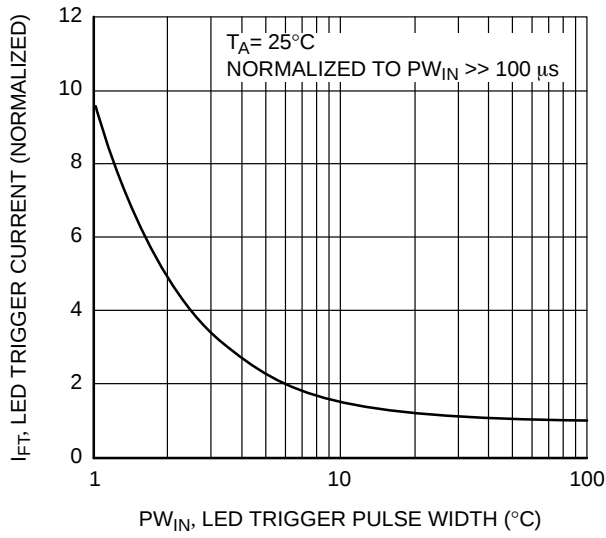


Figure 5. LED Current Required to Trigger vs. LED Pulse Width

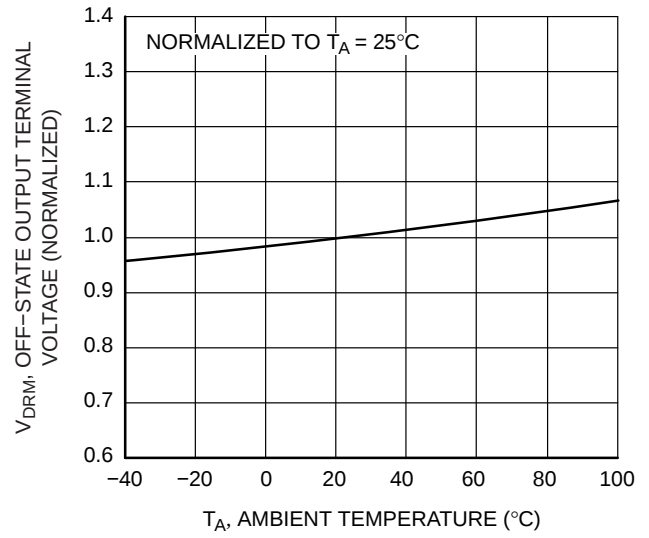


Figure 6. Off-State Output Terminal Voltage vs. Ambient Temperature

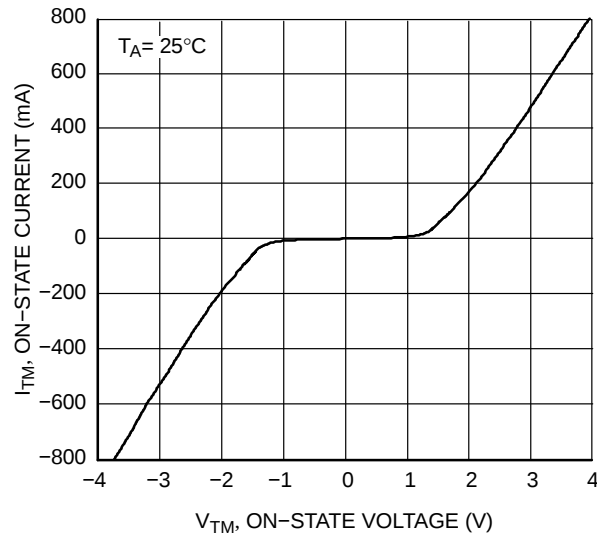
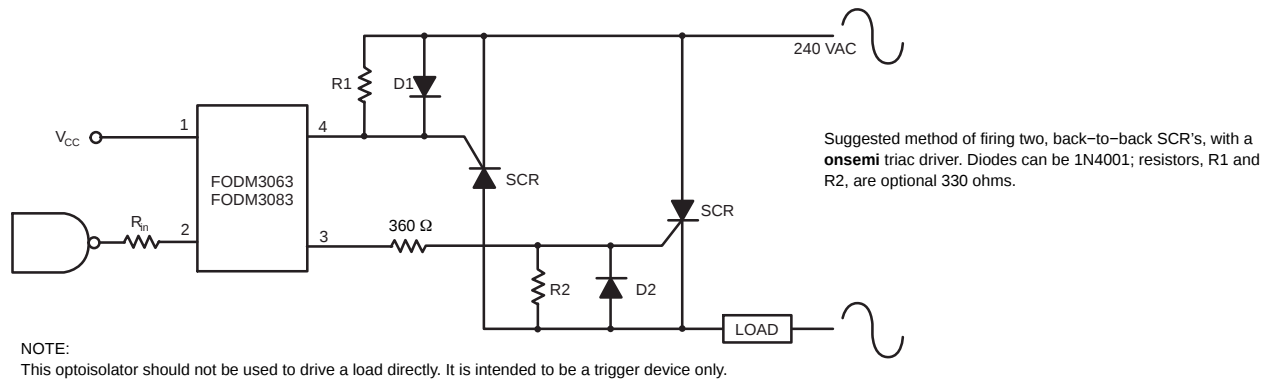


Figure 7. On-State Characteristics

# FODM3063, FODM3083

## TYPICAL APPLICATION INFORMATION



**Figure 8. Inverse-Parallel SCR Driver Circuit (240 VAC)**

## DETERMINING THE POWER RATING OF THE SERIES RESISTORS USED IN A ZERO-CROSS OPTO-TRIAC DRIVER APPLICATION

The following will present the calculations for determining the power dissipation of the current limiting resistors found in an opto-TRIAC driver interface.

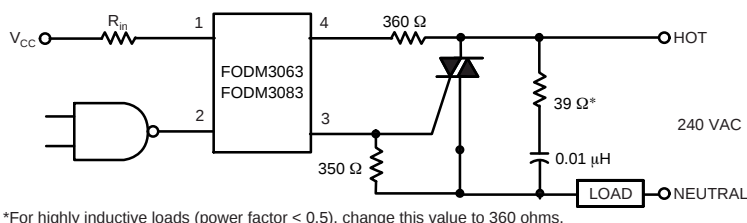
Figure 9 shows a typical circuit to drive a sensitive gate four quadrant power TRIAC. This figure provides typical resistor values for a zero line cross detecting opto-TRIAC when operated from a mains voltage of 20 V to 240 V. The wattage rating for each resistor is not given because their dissipation is dependent upon characteristics of the power TRIAC being driven.

Recall that the opto-TRIAC is used to trigger a four quadrant power TRIAC. Please note that these opto-TRIACs are not recommended for driving “snubberless” three quadrant power TRIACs.

Under normal operation, the opto-TRIAC will fire when the mains voltage is lower than the minimum inhibit trigger voltage, and the LED is driven at a current greater than the maximum LED trigger current. As an example for the FODM3063, the LED trigger current should be greater than 5 mA, and the mains voltage is less than 10 V peak. The inhibit voltage has a typical range of 10 V minimum and 20 V maximum. This means that if a sufficient LED current is flowing when the mains voltage is less than 10 V, the device will fire. If a trigger appears between 10 V and 20 V, the device may fire. If the trigger occurs after the mains voltage has reached 20 V<sub>peak</sub>, the device will not fire.

The power dissipated from resistors placed in series with the opto-TRIAC and the gate of the power TRIAC is much smaller than one would expect. These current handling components only conduct current when the mains voltage is less than the maximum inhibit voltage. If the opto-TRIAC is triggered when the mains voltage is greater than the inhibit voltage, only the TRIAC leakage current will flow. The power dissipation in a 360 Ω resistor shown in Figure 9 is the product of the resistance (360 Ω) times the square of the current sum of main TRIAC's gate current plus the current flowing gate to the MT2 resistor connection (330 Ω). This power calculation is further modified by the duty factor of the duration for this current flow. The duty factor is the ratio of the turn-on time of the main TRIAC to the sine of the single cycle time. Assuming a main TRIAC turn-on time of 50 μs and a 60 Hz mains voltage, the duty cycle is approximately 0.6 %. The opto-TRIAC only conducts current while triggering the main TRIAC. Once the main TRIAC fires, its on-state voltage is typically lower than the on-state sustaining voltage of the opto-TRIAC. Thus, once the main TRIAC fires, the opto-TRIAC is often shunted off. This situation results in very low power dissipation for both the 360 Ω and 330 Ω resistors, when driving a traditional four quadrant power TRIAC.

If a three quadrant “snubberless” TRIAC is driven by the opto-TRIAC, the calculations are different. When the main power TRIAC is driving a high power factor (resistive) load, it shuts off during the fourth quadrant.



Typical circuit for use when hot line switching of 240 VAC is required. In this circuit the “hot” side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

$R_{in}$  is calculated so that  $I_F$  is equal to the rated  $I_{FT}$  of the part, 5 mA. The 39 Ω resistor and 0.01 mF capacitor are for snubbing of the triac and may or may not be necessary depending upon the particular triac and load used.

Figure 9. Hot-Line Switching Application Circuit

If sufficient holding current is still flowing through the opto-TRIAC, the opto-TRIAC will turn-on and attempt to carry the power TRIACs load. This situation typically causes the opto-TRIAC to operate beyond its maximum current rating, and product and resistor failures typically result. For this reason, using an opto-TRIAC to drive a three quadrant “snubberless” power TRIAC is not recommended.

Power in the 360 Ω resistor, when driving a sensitive gate 4 quadrant power TRIAC:

$$I_{GT} = 20 \text{ mA}$$

$$V_{GT} = 1.5 \text{ V}$$

$$DF = 0.6 \%$$

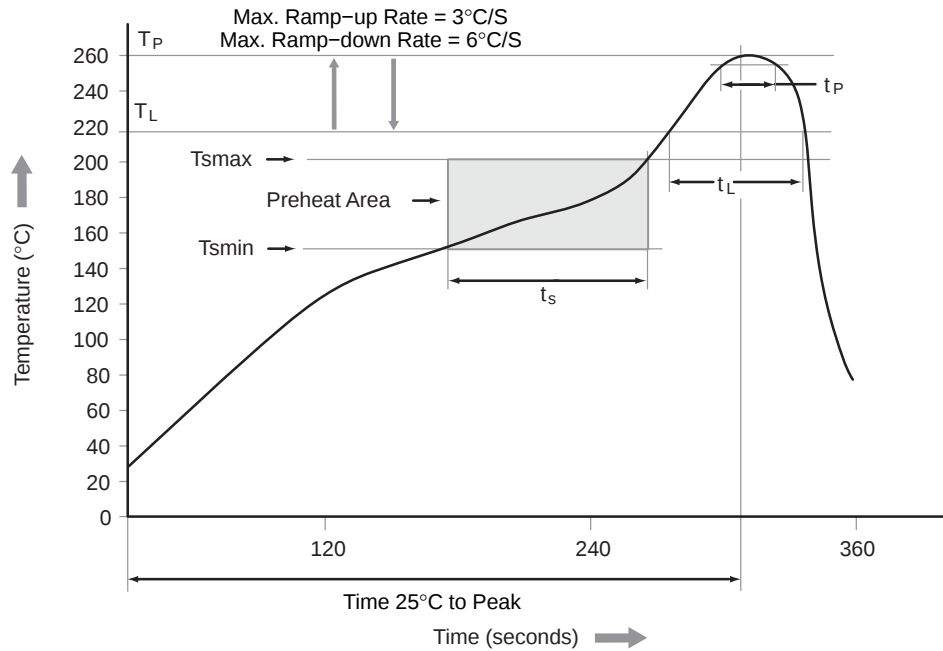
$$P = (I_{GT} + V_{GT} / 330 \Omega)^2 \times 360 \Omega \times DF$$

$$P = (20 \text{ mA} + 1.5 / 330 \Omega)^2 \times 360 \Omega \times 0.6 \% = 1.3 \text{ mW}$$

A 1/4 watt resistor is more than adequate for both the 360 Ω and 330 Ω resistors.

The real power in the snubber resistor is based upon the integral of the power transient present when the load commutes. A fast commuting transient may allow a peak current of 4 A to 8 A in the snubbing filter. For best results, the capacitor should be a non-polarized AC unit with a low ESR. The 39 Ω series resistor sets a time constant and limits the peak current. For a resistive load with a power factor near unity, the commuting transients will be small. This results in a very small peak current given the 0.01 μF capacitor's reactance. Normally, for fractional horse-power reactive loads, the resistor found in the snubber circuit will have a power rating from 1/2 W to 2 W. The resistor should be a low inductance type to adequately filter the high frequency transients.

## REFLOW PROFILE



| Profile Feature                 | Pb-Free Assembly Profile |
|---------------------------------|--------------------------|
| Temperature Min. (Tmin)         | 150°C                    |
| Temperature Max. (Tmax)         | 200°C                    |
| Time (ts) from (Tmin to Tmax)   | 60 – 120 seconds         |
| Ramp-up Rate (tL to tp)         | 3°C/second max.          |
| Liquidous Temperature (TL)      | 217°C                    |
| Time (tL) Maintained Above (TL) | 60–150 seconds           |
| Peak Body Package Temperature   | 260°C +0°C / -5°C        |
| Time (tp) within 5°C of 260°C   | 30 seconds               |
| Ramp-down Rate (TP to TL)       | 6°C/second max.          |
| Time 25°C to Peak Temperature   | 8 minutes max.           |

Figure 10. Reflow Profile

## FODM3063, FODM3083

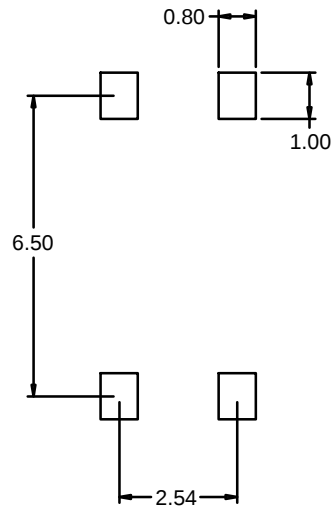
### ORDERING INFORMATION

| Part Number | Package                                                | Shipping <sup>†</sup>    |
|-------------|--------------------------------------------------------|--------------------------|
| FODM3063    | Full Pitch Mini-Flat 4-Pin                             | 100 Units / Tube         |
| FODM3063R2  | Full Pitch Mini-Flat 4-Pin                             | 2500 Units / Tape & Reel |
| FODM3063V   | Full Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option | 100 Units / Tube         |
| FODM3063R2V | Full Pitch Mini-Flat 4-Pin, DIN EN/IEC60747-5-5 Option | 2500 Units / Tape & Reel |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NOTE: The product orderable part number system listed in this table also applies to the FODM3083 products.

### FOOTPRINT DRAWING FOR PCB LAYOUT

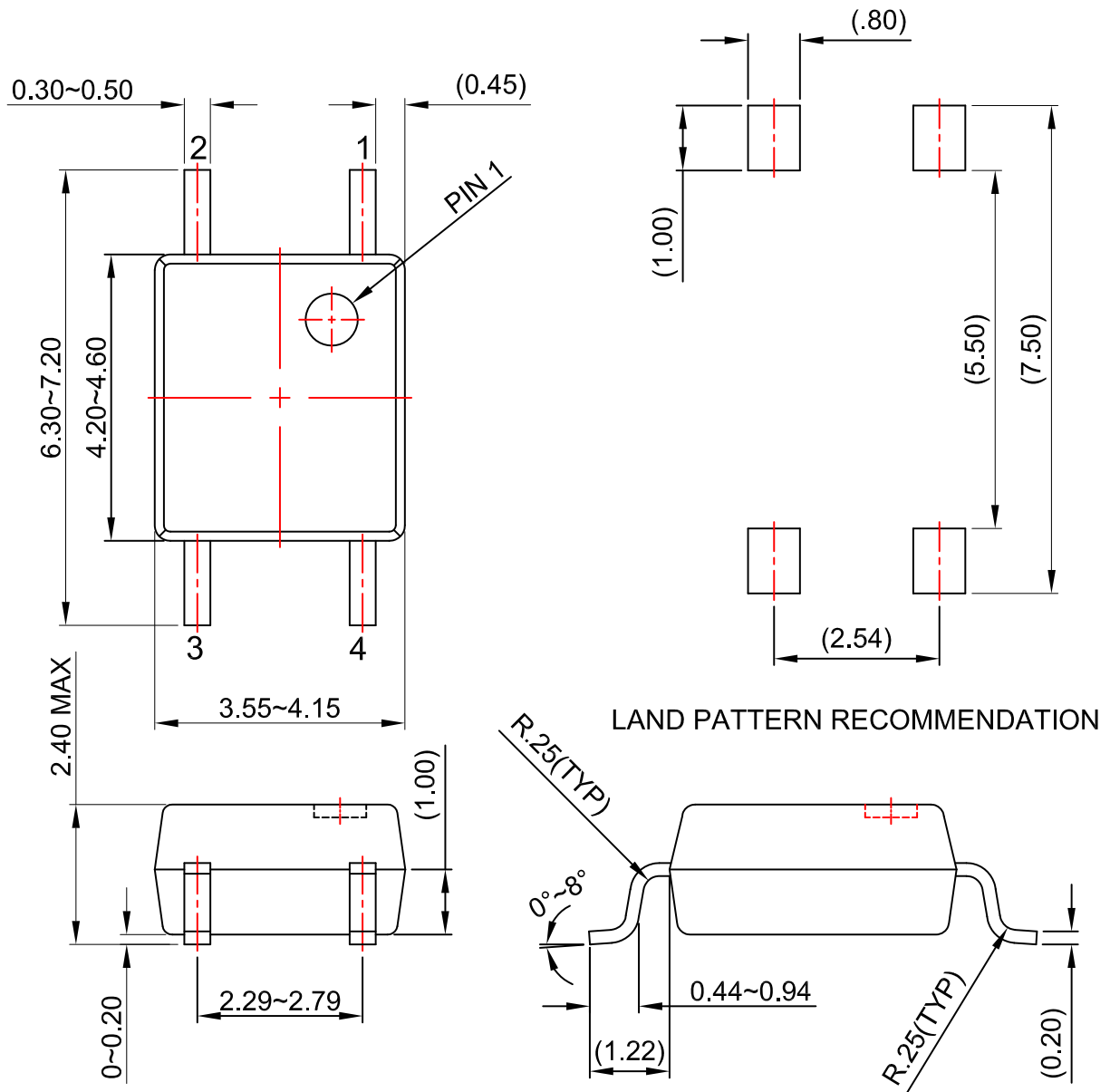


NOTE: All dimensions are in mm.

**Figure 11. Footprint Drawing for PCB Layout**

**MFP4 3.85X4.4, 2.54P**  
CASE 100AP  
ISSUE O

DATE 31 AUG 2016



**NOTES:**

- A) NO STANDARD APPLIES TO THIS PACKAGE.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSION

|                         |                             |                                                                                                                                                                                     |
|-------------------------|-----------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98AON13488G</b>          | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>MFP4 3.85X4.4, 2.54P</b> | <b>PAGE 1 OF 1</b>                                                                                                                                                                  |

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)